

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Jin-Kyoung Jung, *et al.* Examiner: Almo, Khareem E.
Serial No.: 10/799,783 Group Art Unit: 2816
Filing Date: March 12, 2004 Confirmation No.: 8323
Title: INTERNAL VOLTAGE GENERATING CIRCUIT FOR SEMICONDUCTOR
DEVICE

CERTIFICATE OF ELECTRONIC SUBMISSION

I hereby certify that this correspondence and any correspondence referred to as being attached or enclosed is being sent by electronic submission to the United States Patent and Trademark Office on the date indicated next to my name.

10/10/2007
Date


Christina Harris

Via Electronic Submission
Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

TRANSMITTAL LETTER

Sir:

Enclosed herewith for filing in the above-identified patent application please find the following listed items:

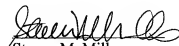
1. Amendment After Final Rejection in response to Final Office Action dated August 10, 2007.

In connection with the foregoing matter, please charge any additional fees which may be due, or credit any overpayment, to Deposit Account Number 50-1798.

Respectfully submitted,

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